



ClockBuilder Pro Wizard

We Make Timing Simple



Work With a Design

[Create New Project](#)

[Open Project](#)

[Convert Existing Project/NVM File](#)

[Open Sample Project](#)

No EVB Present
No evaluation board has been detected.

Quick Links

[Skyworks Timing Solutions Knowledge Base](#)
[Custom Part Number Lookup](#)

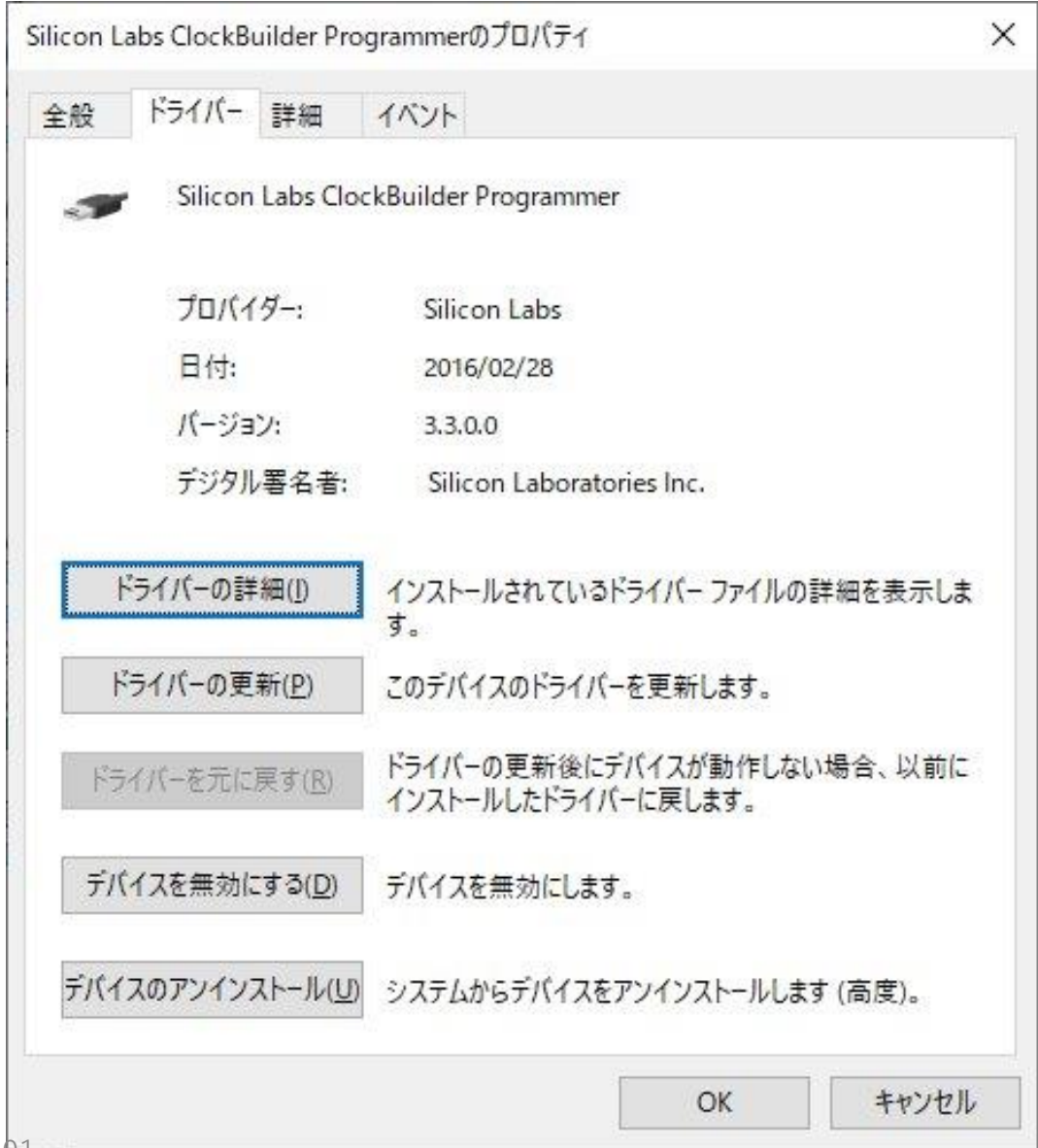
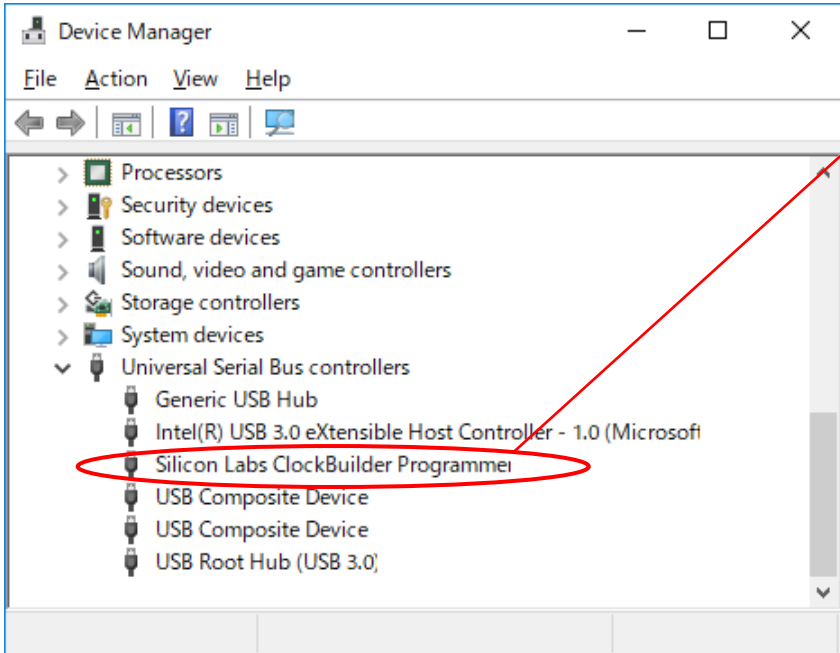
Applications Documentation

[10/40/100G Line Card Whitepaper](#)
[Clock Generators for Cloud Data Centers](#)
[Optimizing Jitter Performance](#)
[Selecting the Right Clocks for Timing Synchronization Applications](#)
[PCIe Gen 4.0 Jitter Requirements](#)
[Selecting a PCIe Reference Clock Source](#)
[Making Accurate Clock Jitter Measurements](#)

ClockBuilder Pro Documentation

[CBPro Overview](#)
[CBPro Tools & Support for In-System Programming](#)
[CLI User's Guide](#)
[Release Notes](#)







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Field Programmer Detected
Field Programmer

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CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: --- Select Project ... Clear

Project File Created By: --- OPN Lookup

Project Part: ---

Project Design ID: ---

Project Design Check: ---

Project File NVM Hash: --- ?

Device Part Number: --- Scan for Device Clear

Device Design ID: ---

Device NVM State: ---

Valid Burns: 0

Burns with Error: 0

Program NVM



- [CBPro Overview](#)
- [CBPro Tools & Support for In-System Programming](#)
- [CLI User's Guide](#)
- [Release Notes](#)

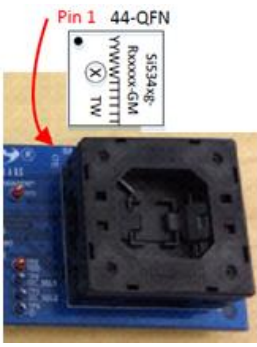
ClockBuilder Pro Wizard

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CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: C:\Users\93256\Documents\Timing team\NVM書き込み手順\Si5394-RevA-example-20230314\Project.slabtimeproj Select Project ... Clear

Project File Created By: CBPro v4.8 OPN Lookup

Project Part: Si5394 Rev A

Project Design ID: (empty)

Project Design Check: OK

Project File NVM Hash: 0x1C3A54737ABCE7174F75C28022280FF8 ([copy to clipboard](#)) ?

Device Part Number: Present, Si5394D-A-GM Scan for Device Clear

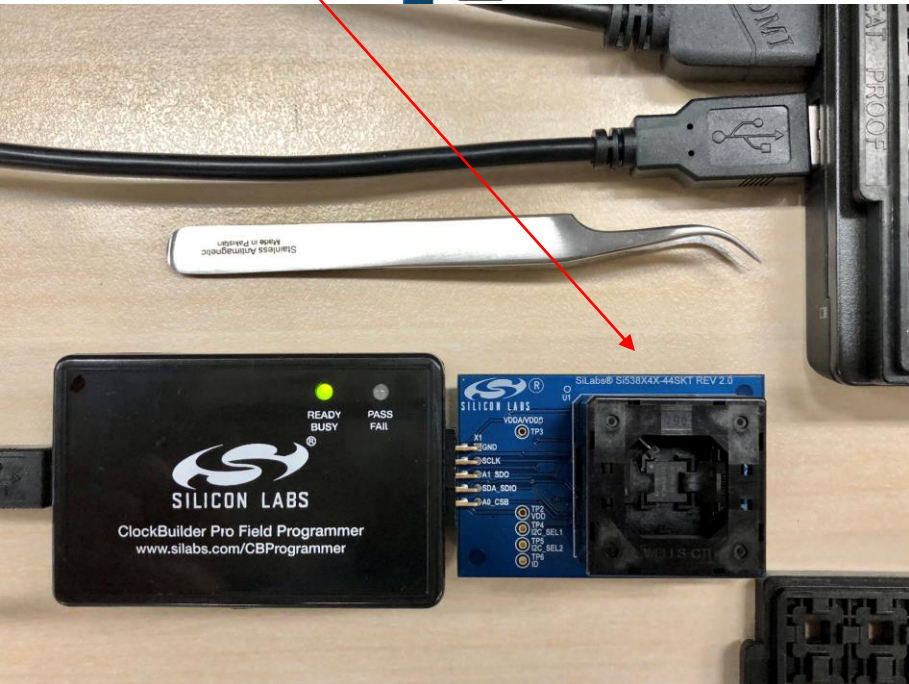
Device Design ID: 5394DBP1

Device NVM State: 1 bank used, 2 banks available for burn

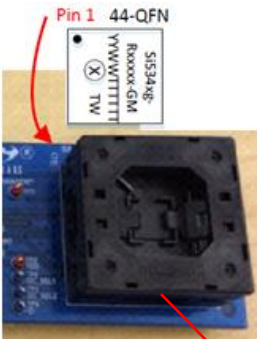
Valid Burns: 0

Burns with Error: 0

Program NVM



- [CBPro Overview](#)
- [CBPro Tools & Support for In-System Programming](#)
- [CLI User's Guide](#)
- [Release Notes](#)



ClockBuilder Pro Wizard - Skyworks

ClockBuilder Pro Wizard

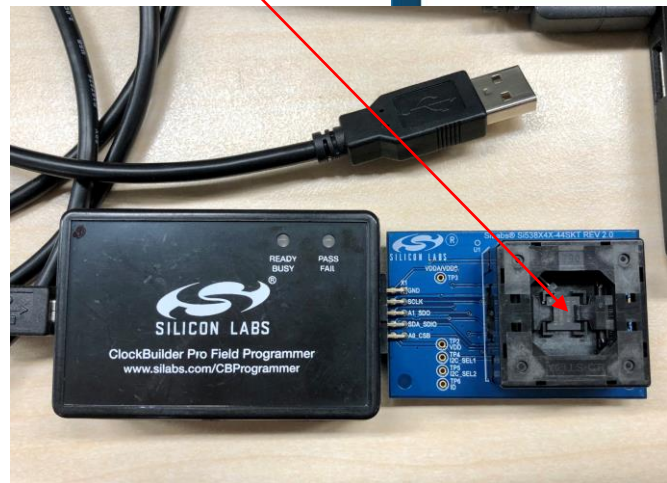
We Make Timing Simple

Work With a Design

Quick Links

[Create New Project](#)

[Skyworks Timing Solutions Knowledge Base](#)



NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device:

Project File:

Project File Created By: CBPro v4.8

Project Part: Si5394 Rev A

Project Design ID: (empty)

Check: OK

Hash: 0x1C3A54737ABCE7174F75C28022280FF8 ([copy to clipboard](#)) ?

Device: Present, Si5394D-A-GM

5394DBP1

1 bank used, 2 banks available for burn

0
0





ClockBuilder Pro Wizard



We Make Timing Simple



Work With

- Cre
- Op
- Cor
- ex Op
- Field

CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: C:\Users\93256\Documents\Timing team\NVM書き込み手順\Si5394-RevA-example-20230314\Project.slabtimeproj Select Project ... Clear

Project File Created By: CBPro v4.8 OPN Lookup

Project Part: Si5394 Rev A

Project Design ID: (empty)

Project Design Check: OK

Project File NVM Hash: 0x1C3A54737ABCE7174F75C28022280FF8 ([copy to clipboard](#)) ?

Device Part Number: Present, Si5394D-A-GM Scan for Device Clear

Device Design ID: 5394DBP1

Device NVM State: 1 bank used, 2 banks available for burn

Valid Burns: 0

Burns with Error: 0

Program NVM

[CLI User's Guide](#)
[Release Notes](#)





ClockBuilder Pro Wizard

We Make Timing Simple



Work With

- 📄 Create
- 📁 Open
- ➡️ Connect
- 🔧 Execute
- 🔌 Field

CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: C:\Users\93256\Documents\Timing team\NVM書き込み手順

Project File C: [Select Project...] [Clear]

Project Part: [Pickup]

Project Design: [Progress Bar]

Project Design: [Progress Bar]

Project File N: [Progress Bar]

Device Part N: [Progress Bar] [Clear]

Device Design: [Progress Bar]

Device NVM State: 1 bank used, 2 banks available for burn

Valid Burns: 0

Burns with Error: 0

[Program NVM]

CB NVM Program Progress

Validating volatile write via read-back ...

[Progress Bar]

[CLI User's Guide](#)

[Release Notes](#)





ClockBuilder Pro Wizard

CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: C:\Users\93256\Documents\Timin
 ¥Si5394-RevA-example-20230314P

Project File Created By: CBPro v4.8

Project Part: Si5394 Rev A

Project Design ID: (empty)

Project Design Check: OK

Project File NVM Hash: 0x1C3A54737ABCE7174F75C28022

Device Part Number: Present, Si5394D-A-GM

Device Design ID: 5394DBP1

Device NVM State: 2 banks used, 1 bank available for burn

Valid Burns: 1

Burns with Error: 0

Program NVM

CB NVM Program Success



Configuration burned to NVM and verified via read-back.

OK

Scan for Device

Clear





ClockBuilder Pro Wizard



CB NVM Program Tool - ClockBuilder Pro v4.8

Field Programmer Mode: Socket, Si534x/7x/9x-QFN44

Target Device: Si534x/7x/8x/9x (not firmware based)

Project File: C:\Users\93256\Documents\Timing team\NVM書き込み手順
Si5394-RevA-example-20230314Project.slabttimeproj

Select Project ...

Clear

Project File Created By: CBPro v4.8

OPN Lookup

Project Part: Si5394 Rev A

Project Design ID: (empty)

Project Design Check: OK

Project File NVM Hash: 0x1C3A54737ABCE7174F75C28022280FF8 ([copy to clipboard](#)) ?

Device Part Number: Present, Si5394D-A-GM

Scan for Device

Clear

Device Design ID: 5394DBP1

Device NVM State: 2 banks used, 1 bank available for burn

Valid Burns: 0

Burns with Error: 0

Program NVM



ClockBuilder Pro Wizard

We Make Timing Simple

Work With a Design

- Create New Design
- Open Design Project
- Open Sample Design
- No EVB Present
No evaluation board

Quick Links

- Clock Generators & Jitter Attenuators
- Knowledge Base
- Custom Part Number Lookup
- SyncE and IEEE 1588 App Note

ClockBuilder Pro Documentation

- CBPro Overview
- Command Line Interface: [Overview](#) • [User's Guide](#)
- [Release Notes](#) • [Knowledge Base](#)

Version 2.8
Built on 4/24/2016

CB ClockBuilder Pro Update Available

A new version of ClockBuilder Pro is available for download!

Current version you have installed: 2.8
New version available: 3.4

A list of new features and fixes is available in the [release notes](#).

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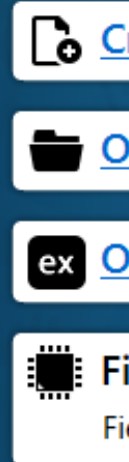


ClockBuilder Pro Wizard

We Make Timing Simple



Work V



CB Field Programmer - Burn NVM

Mode: Kit socket board
 SPI 4-Wire @ 12 MHz, 3.3V Used

Wired to your own board
 I2C Address 0x68 / 104d; 400 kHz; 3.3V

Socket Board: Present, QFN44
 DUT: Present, Si5340 Rev D
 Design ID (on DUT): NEC53401
 NVM: **3 banks used, 0 banks available for burn**
 Project File: ---

Buttons: Help, OPN Lookup, Debug Options ..., Select Project ..., Scan, Burn NVM

CB NVM Burn Progress

Configuration burned to NVM and verified via read-back.

OK

Ver

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ClockBuilder Pro Wizard



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Field Programmer Detected

Field Programmer [NVM Program Tool](#) [EVB GUI](#)

Quick Links

- [Skyworks Timing Solutions Knowledge Base](#)
- [Custom Part Number Lookup](#)

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- [Release Notes](#)



CB Field Programmer - ClockBuilder Pro
_ □ ×

File Help
▾

Info
DUT Settings Editor
DUT Register Editor
Status

Eng Name	Value Read	Value to Write	Location	Type	
✕ <input style="width: 95%;" type="text"/>	---	---	---	---	<input type="button" value="Read"/> <input type="button" value="Write"/>

Click the 'Add Setting' button to add another register setting (aka bitfield) to view or edit.

Field Programmer

Family: Si534x/7x/8x/9x (not firmware based)

Target: Socket, Si534x/7x/9x-QFN44

Socket Power: Off

Interface: SPI 4-Wire; 12 MHz; 3.3V

Part Number ---

Design ID ---

Device Control / Misc

Device control will be available once a

Log
Filtered ▾
Auto Scroll: On ▾

Timestamp	Source	Message
15:44:28.015	EVB	Finished Read_ADC(num_samples=10) => 691.3
15:44:28.063	EVB	Finished Check_Socket_And_DUT_State() => Socket: QFN44, Orientation: Safe
15:44:28.063	EVB	Starting Set_Voltage_Regulator_Enable(regulator=VDD, enabled=False)
15:44:28.063	EVB	Starting Set_MCU_Signal(signal_id=VDD_SHDNB, state=Logic0)
15:44:28.067	EVB	Starting Set_Voltage_Regulator_Enable(regulator=VDDA_VDDS, enabled=False)
15:44:28.067	EVB	Starting Set_MCU_Signal(signal_id=VDDA_VDDS_SHDNB, state=Logic0)
15:44:28.073	EVB	Starting Check_Socket_And_DUT_State()

EVB Firmware 0.84 | Field Programmer: Socket, Si534x/7x/9x-QFN44; Si534x/7x/8x/9x (not firmware based); SPI 4-Wire; 12 MHz; 3.3V
ClockBuilder Pro v4.6 [2022-10-07]

Socket PowerをONにする事で
アクセス可能となります。

CB Field Programmer - ClockBuilder Pro
_ □ ×

File Help

Info

DUT Settings Editor

DUT Register Editor

Status

Register Peek/Poke

Hex

Decimal

Address:

Bytes: Read Write

Hex:

Unsigned Int:

Binary: 15 14 13 12 | 11 10 9 8 | 7 6 5 4 | 3 2 1 0
0 0 0 0 | 0 0 0 0 | 0 0 0 0 | 0 0 0 0

(binary edit is only supported with 16 bits or less)

Log

Filtered ▾
Auto Scroll: On ▾
Insert Marker
Clear
Copy to Clipboard
Pause

Timestamp	Source	Message
15:45:55.578	EVB	Finished Read_DUT_Byte(address=0x0278) => 0x00
15:45:55.579	EVB	Starting Read_DUT_Byte(address=0x0006)
15:45:55.620	EVB	Finished Read_DUT_Byte(address=0x0006) => 0x00
15:45:55.620	EVB	Starting Read_DUT_Byte(address=0x0007)
15:45:55.652	EVB	Finished Read_DUT_Byte(address=0x0007) => 0x06
15:45:55.652	EVB	Starting Read_DUT_Byte(address=0x0008)
15:45:55.731	EVB	Finished Read_DUT_Byte(address=0x0008) => 0x08

Field Programmer

Family: Si534x/7x/8x/9x (not firmware based)

Target: Socket, Si534x/7x/9x-QFN44

Socket Power: On

Interface: SPI 4-Wire; 12 MHz; 3.3V

Part Number Si5394D-A-GM

Design ID example1

Config
Scan

Device Control / Misc

Soft Reset and Calibration

SOFT_RST_ALL

SOFT_RST

Hard Reset, Sync, & Power Down

HARD_RST

SYNC

PDN:

Frequency Adjust

FINC

FDEC

EVB Firmware 0.84 | Field Programmer: Socket, Si534x/7x/9x-QFN44; Si534x/7x/8x/9x (not firmware based); SPI 4-Wire; 12 MHz; 3.3V
ClockBuilder Pro v4.6 [2022-10-07]

CB Field Programmer - ClockBuilder Pro
_ □ ×

File Help

Info

DUT Settings Editor

DUT Register Editor

Status

Field Programmer Identification:

Serial Number:

DUT ID Registers:

DEVICE_PN_BASE:

DIE_REV:

DEVICE_REV:

TEMP_GRADE:

PKG_ID:

BASELINE_ID:

DEVICE_GRADE:

OPN_ID:

OPN_REVISION:

DESIGN_ID:

TOOL_VERSION:

Log

Filtered ▼ Auto Scroll: On ▼ Insert Marker Clear Copy to Clipboard Pause

Timestamp	Source	Message
15:45:55.578	EVB	Finished Read_DUT_Byte(address=0x0278) => 0x00
15:45:55.579	EVB	Starting Read_DUT_Byte(address=0x0006)
15:45:55.620	EVB	Finished Read_DUT_Byte(address=0x0006) => 0x00
15:45:55.620	EVB	Starting Read_DUT_Byte(address=0x0007)
15:45:55.652	EVB	Finished Read_DUT_Byte(address=0x0007) => 0x06
15:45:55.652	EVB	Starting Read_DUT_Byte(address=0x0008)
15:45:55.731	EVB	Finished Read_DUT_Byte(address=0x0008) => 0x08

Field Programmer

Family: Si534x/7x/8x/9x (not firmware based)

Target: Socket, Si534x/7x/9x-QFN44

Socket Power: On

Interface: SPI 4-Wire; 12 MHz; 3.3V

Part Number Si5394D-A-GM

Design ID example1

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Device Control / Misc

Soft Reset and Calibration

SOFT_RST_ALL
SOFT_RST

Hard Reset, Sync, & Power Down

HARD_RST
SYNC

PDN: 0

Frequency Adjust

FINC
FDEC

EVB Firmware 0.84 | Field Programmer: Socket, Si534x/7x/9x-QFN44; Si534x/7x/8x/9x (not firmware based); SPI 4-Wire; 12 MHz; 3.3V

ClockBuilder Pro v4.6 [2022-10-07]

Register Peek/Poke

Hex Decimal

Address:

Bytes:

Hex:

Unsigned Int:

Binary: |

(binary edit is only supported with 16 bits or less)

Log

Filtered

Timestamp	Source	Message
15:45:55.620	EVB	Starting Read_DUT_Byte(address=0x0007)
15:45:55.652	EVB	Finished Read_DUT_Byte(address=0x0007) => 0x06
15:45:55.652	EVB	Starting Read_DUT_Byte(address=0x0008)
15:45:55.731	EVB	Finished Read_DUT_Byte(address=0x0008) => 0x08
15:52:25.258	Other	Starting status register polling
15:56:31.134	EVB	Starting Read_DUT_Bytes(address=0x0000, num_bytes=1)
15:56:31.185	EVB	Finished Read_DUT_Bytes(address=0x0000, num_bytes=1) => 0x06

Field Programmer

Family: Si534x/7x/8x/9x (not firmware based)

Target: Socket, Si534x/7x/9x-QFN44

Socket Power: On

Interface: SPI 4-Wire; 12 MHz; 3.3V

Part Number Si5394D-A-GM

Design ID example1

Device Control / Misc

Soft Reset and Calibration

Hard Reset, Sync, & Power Down

PDN: 0

Frequency Adjust

Auto Poll Registers ?

	Inputs	DSPLL	Other
Non-Sticky	<input type="button" value="LOS[0]"/>	<input type="button" value="LOL"/>	<input type="button" value="SYSINCAL"/>
	<input type="button" value="LOS[1]"/>	<input type="button" value="HOLD"/>	<input type="button" value="SMBUS_TIMEOUT"/>
	<input type="button" value="LOS[2]"/>	<input type="button" value="CAL_PLL"/>	
	<input type="button" value="LOS[3]"/>	<input type="button" value="HOLD_HIST_VALID"/>	
	<input type="button" value="LOSXAXB"/>	<input type="button" value="FASTLOCK_STATUS"/>	
	<input type="button" value="OOF[0]"/>		
	<input type="button" value="OOF[1]"/>		
	<input type="button" value="OOF[2]"/>		

Log

Timestamp	Source	Message
15:45:55.652	EVB	Finished Read_DUT_Byte(address=0x0007) => 0x06
15:45:55.652	EVB	Starting Read_DUT_Byte(address=0x0008)
15:45:55.731	EVB	Finished Read_DUT_Byte(address=0x0008) => 0x08
15:52:25.258	Other	Starting status register polling
15:56:31.134	EVB	Starting Read_DUT_Bytes(address=0x0000, num_bytes=1)
15:56:31.185	EVB	Finished Read_DUT_Bytes(address=0x0000, num_bytes=1) => 0x06
15:56:53.712	Other	Starting status register polling

Field Programmer

Family: Si534x/7x/8x/9x (not firmware based)
 Target: Socket, Si534x/7x/9x-QFN44
 Socket Power: On Off
 Interface: SPI 4-Wire; 12 MHz; 3.3V
 Part Number: Si5394D-A-GM
 Design ID: example1

Device Control / Misc

Soft Reset and Calibration

Hard Reset, Sync, & Power Down

PDN: 0 1

Frequency Adjust