



Instant-DevKit Arria 10 FPGA FMC IDK

The Arria 10 FPGA FMC Instant-Development Kit provides to developers the best Out-Of-The box experience, combining the Best-In Class compact hardware platform and the most efficient intuitive software environment.

Target markets :

High speed Data communication - High Performance Computing - Finance
Video capture and processing - Industrial - Military - Test & Measurement - Medical

DevKitInvasion



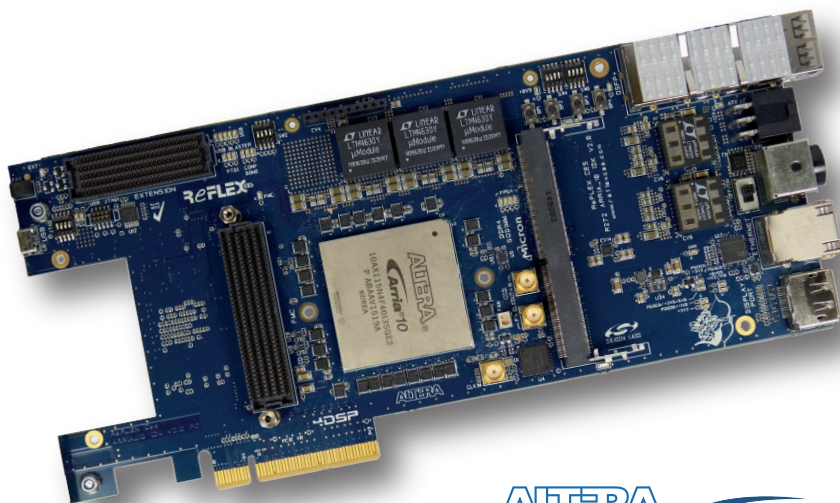
ATTILA
Arria 10 FPGA FMC IDK



ALARIC
Arria 10 SoC FMC IDK



ACHILLES
Arria 10 SoC SoM IDK



ALTERA
now part of Intel

Arria[®]10



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Featured Device

Arria 10 GX 1150 KLEs

- Board based on ALTERA FPGA 1150 KLEs, speed grade -3 by default
- PCIe edge connector for Gen3 x8 (64Gb/s)
- FMC High Pin Count fully compliant:
 - 168 LVCMOS (1.8V) or 84 LVDS (1.8V, 2.5V)
 - 10 XCVR (Typical 10Gb/s)
- Extension connector SEARAY™ Series
 - 20 LVDS (1.8V) + 1 LVDS Clock
 - 22 XCVR (up to 12.5Gb/s per link)
- QSFP+ optical cage (4 XCVR : 12.5 Gb/s per link)
- One serial over USB High speed link (through USB Hub)

- RJ45 copper connector 10/100/1000Base-T Ethernet (Through RGMII PHY)
- DDR4 SODIMM interface (up to 16 GB, speeds up to 1200 MHz/2400 MT/s depends on FPGA speed grade, support ECC and Non ECC)
- DisplayPort Rev1.2 (up to 5.4Gbit/s)
- MAX 10 Power supplies monitoring with its ADCs solution
- Innovative clock tree (Silicon Labs)
- +12V on a Jack/EdgePCle/PCle Aux connector
- Active Heat sink
- PCIe Bracket, LED

Board Programming

- JTAG : On-board USB-Blaster II MAX V (reachable by USB Hub)
- Fast passive parallel (FPP) configuration via MAX@10 device and flash memory
- AS configuration supported with NOR FLASH EPCQL1024

Board Size

- Length : 240 mm (9.45 inches)
- Height : 111.15 mm (4.38 inches) / Standard Height PCIe
- PCB thickness : 1.57 mm (0.062 inches) / Standard Thickness PCIe

Power & Environmental

- Power : 120 W max / 65W typical
- Environmental : Commercial grade 0°C / +60°C

Kit Ordering information

- Development Kit / RXCA10X115PF40-IDK00A (Production version)

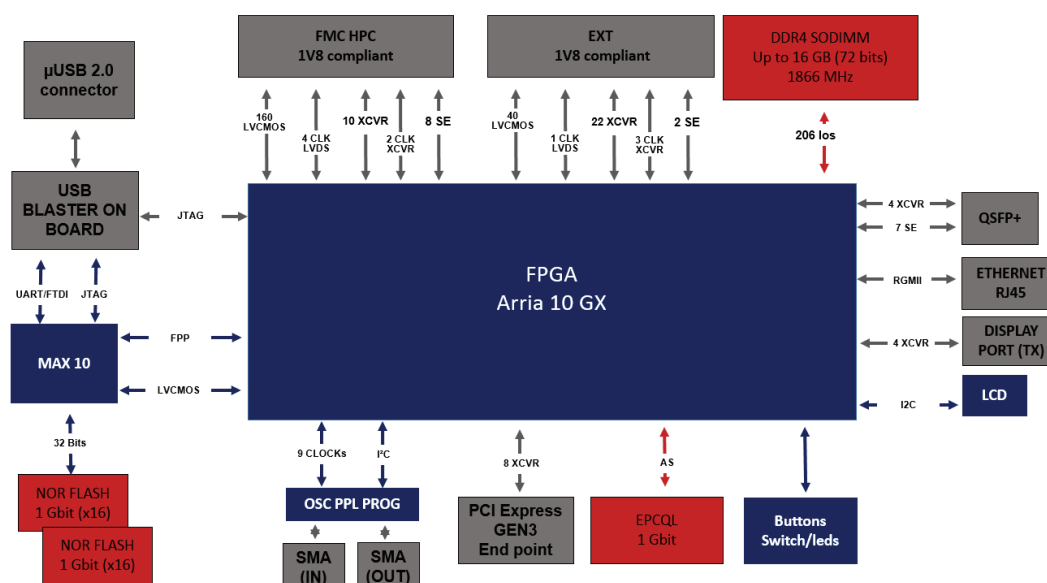
Clock Circuitry

- 3 x SMA connectors (1 main input clock + 1 diff. output programmable clock)
- 9 x FPGA XCVR programmable inputs Clocks (up to 800 MHz to reach different communication protocol)

Kit Content

- Power adapter (US, UK, EU, JP) and cables (USB, Ethernet, DisplayPort, SMA)
- 4GB non-ECC DDR4 SODIMM module (Micron Technology)
- FMC, Extension connector and PCIe Loopback boards
- LCD (2 lines x 16 characters, I2C, 5V)
- Starter Guide
- ReFLEX CES innovative software interface (GUI) on USB Key/Windows64 with Test Designs
- OpenCL BSP HPC
- Schematics and assembly documentation

Partners



Contact us :

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